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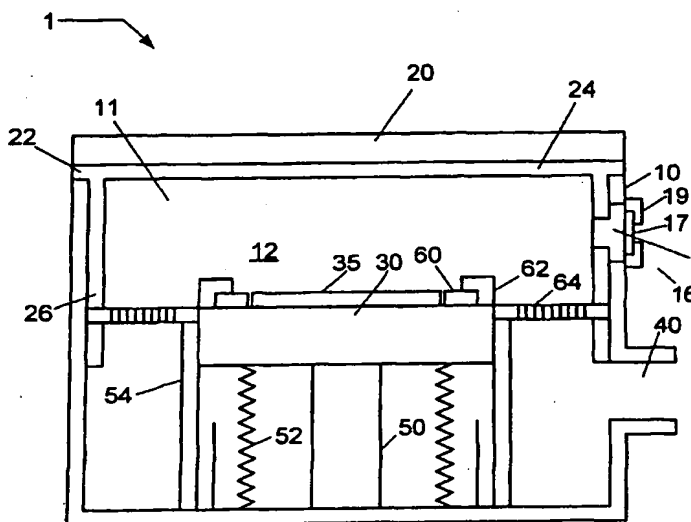
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[Continued on next page]

(54) Title: METHOD AND APPARATUS FOR AN IMPROVED BAFFLE PLATE IN A PLASMA PROCESSING SYSTEM



(57) Abstract: The present invention presents an improved baffle plate for plasma processing system, wherein the design and fabrication of the baffle plate advantageously provides for a uniform processing plasma in the process space with substantially minimal erosion of the baffle plate.



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*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

## METHOD AND APPARATUS FOR AN IMPROVED BAFFLE PLATE IN A PLASMA PROCESSING SYSTEM

### Cross-reference to Related Applications

**[0001]** This application is related to co-pending United States patent application serial no. 10/XXX,XXX, entitled "Method and apparatus for an improved upper electrode plate with deposition shield in a plasma processing system", Attorney docket no. 226272US6YA, filed on even date herewith; co-pending United States patent application serial no. 10/XXX,XXX, entitled "Method and apparatus for an improved upper electrode plate in a plasma processing system", Attorney docket no. 225277US6YA, filed on even date herewith; co-pending United States patent application serial no. 10/XXX,XXX, entitled "Method and apparatus for an improved baffle plate in a plasma processing system", Attorney docket no. 228411US6YA, filed on even date herewith; co-pending United States patent application serial no. 10/XXX,XXX, entitled "Method and apparatus for an improved deposition shield in a plasma processing system", Attorney docket no. 226275US6YA, filed on even date herewith; co-pending United States patent application serial no. 10/XXX,XXX, entitled "Method and apparatus for an improved optical window deposition shield in a plasma processing system", Attorney docket no. 226276US6YA, filed on even date herewith; and co-pending United States patent application serial no. 10/XXX,XXX, entitled "Method and apparatus for an improved bellows shield in a plasma processing system", Attorney docket no. 226277US6YA, filed on even date herewith. The entire contents of all of those applications are herein incorporated by reference in their entirety.

### Field of the Invention

**[0002]** The present invention relates to an improved component for a plasma processing system and, more particularly, to a baffle plate employed in a plasma processing system surrounding a substrate holder.

### Background of the Invention

**[0003]** The fabrication of integrated circuits (IC) in the semiconductor industry typically employs plasma to create and assist surface chemistry within a plasma reactor necessary to remove material from and deposit material to a substrate. In general, plasma is formed within the plasma reactor under vacuum conditions by heating electrons to energies sufficient to sustain ionizing collisions with a supplied process gas. Moreover, the heated electrons can have energy sufficient to sustain dissociative collisions and, therefore, a specific set of gases under predetermined conditions (e.g., chamber pressure, gas flow rate, etc.) are chosen to produce a population of charged species and chemically reactive species suitable to the particular process being performed within the chamber (e.g., etching processes where materials are removed from the substrate or deposition processes where materials are added to the substrate).

**[0004]** Although the formation of a population of charged species (ions, etc.) and chemically reactive species is necessary for performing the function of the plasma processing system (i.e. material etch, material deposition, etc.) at the substrate surface, other component surfaces on the interior of the processing chamber are exposed to the physically and chemically active plasma and, in time, can erode. The erosion of exposed components in the plasma processing system can lead to a gradual degradation of the plasma processing performance and ultimately to complete failure of the system.

**[0005]** In order to minimize the damage sustained by exposure to the processing plasma, components of the plasma processing system, known to sustain exposure to the processing plasma, are coated with a protective barrier. For example, components fabricated from aluminum can be anodized to produce a surface layer of aluminum oxide, which is more resistant to the plasma. In another example, a consumable or replaceable component, such as one fabricated from silicon, quartz, alumina, carbon, or silicon carbide, can be inserted within the processing chamber to protect the surfaces of more valuable components that would impose greater costs during frequent replacement. Furthermore, it is desirable to select surface materials that minimize the introduction of unwanted contaminants, impurities, etc. to the processing plasma and possibly to the devices formed on the substrate.

**[0006]** In both cases, the inevitable failure of the protective coating, either due to the integrity of the protective barrier or the integrity of the fabrication of the protective barrier, and the consumable nature of the replaceable components demands frequent maintenance of the plasma processing system. This frequent maintenance can produce costs associated with plasma processing down-time and new plasma processing chamber components, which can be excessive.

#### Summary of the Invention

**[0007]** The present invention provides an improved baffle plate for a plasma processing system, wherein the design and fabrication of the baffle plate advantageously addresses the above-identified shortcomings.

**[0008]** It is an object of the present invention to provide a baffle plate comprising a ring having an upper surface, a lower surface, an inner radial edge, and an outer radial edge. The upper surface can further comprise a first mating surface proximate the outer radial edge. The lower surface can further comprise a second mating surface proximate the outer radial edge and a plurality of fastener mating surfaces proximate the inner radial edge. The baffle plate can further comprise at least one passageway coupled to the upper surface and to the lower surface, and configured to permit the flow of gas therethrough, wherein the at least one passageway can comprise an inner passageway surface.

**[0009]** It is an object of the present invention to further provide a baffle plate comprising a plurality of fastening receptors, each fastening receptor coupled to the upper surface and the lower surface, and configured to receive fastening devices to couple the baffle plate to the plasma processing system. Each fastening receptor can comprise an entrant cavity, an exit through-hole, and an inner receptor surface.

**[0010]** It is an object of the present invention to further provide a baffle plate comprising a plurality of mounting through-holes, each mounting through-hole coupled to the upper surface and the lower surface, and configured to receive fastening devices to couple the baffle plate to the plasma processing system.

**[0011]** It is an object of the present invention that the baffle plate further comprises a protective barrier formed on exposed surfaces of the baffle plate facing the processing plasma.

**[0012]** It is a further object of the present invention that the exposed surfaces of the baffle plate comprise the upper surface of the baffle plate excluding the first mating surface; the lower surface of the baffle plate excluding the plurality of fastener mating surfaces and the second mating surface; and the inner passageway surface contiguous with the upper surface and the lower surface. The present invention optionally can include also identifying the first entrant surface of each of the plurality of fastening receptors and the first lip surface of each of the plurality of fastening receptors as exposed surfaces.

**[0013]** The present invention further provides a method of producing the baffle plate in the plasma processing system comprising the steps: fabricating the baffle plate; anodizing the baffle plate to form a surface anodization layer on the baffle plate; machining exposed surfaces on the baffle plate to remove the surface anodization layer; and forming a protective barrier on the exposed surfaces.

**[0014]** The present invention provides another method of producing the baffle plate in the plasma processing system comprising the steps: fabricating the baffle plate; masking exposed surfaces on the baffle plate to prevent formation of a surface anodization layer; anodizing the baffle plate to form the surface anodization layer on the baffle plate; unmasking the exposed surfaces; and forming a protective barrier on the exposed surfaces.

**[0015]** The present invention provides another method of producing the baffle plate in the plasma processing system comprising the steps: fabricating the baffle plate; and forming a protective barrier on a plurality of exposed surfaces.

**[0016]** The present invention may also include a process of combining machining and masking to prepare the exposed surfaces to receive the protective barrier, and then forming the protective barrier on the exposed surfaces. For example, two of the exposed surfaces can be masked prior to anodizing, and two of the surfaces can be machined after anodizing to create four exposed surfaces on which the protective barrier can be formed.

**[0017]** Any of the above methods may also optionally include machining anodized (or otherwise coated) surfaces that are not exposed surfaces (e.g., to obtain a bare metal connection where the machined surface will mate with another part).

### Brief Description of the Drawings

**[0018]** These and other advantages of the invention will become more apparent and more readily appreciated from the following detailed description of the exemplary embodiments of the invention taken in conjunction with the accompanying drawings, where:

**[0019]** FIG. 1 shows a simplified block diagram of a plasma processing system comprising a baffle plate according to an embodiment of the present invention;

**[0020]** FIG. 2 shows a plan view of a baffle plate for a plasma processing system according to an embodiment of the present invention;

**[0021]** FIG. 3 shows a cross sectional view of a baffle plate for a plasma processing system according to an embodiment of the present invention;

**[0022]** FIG. 4 shows an expanded cross sectional view taken along a minor axis of one passageway formed within a baffle plate for a plasma processing system according to an embodiment of the present invention;

**[0023]** FIG. 5A shows an expanded view of one passageway formed within an upper surface of a baffle plate for a plasma processing system according to an embodiment of the present invention;

**[0024]** FIG. 5B shows an expanded cross sectional view taken along a major axis of one passageway formed within a baffle plate for a plasma processing system according to an embodiment of the present invention;

**[0025]** FIG. 5C shows an expanded view of one passageway formed within a lower surface of a baffle plate for a plasma processing system according to an embodiment of the present invention;

**[0026]** FIG. 6 presents an expanded view of an inner radial edge of a baffle plate for a plasma processing system according to an embodiment of the present invention;

**[0027]** FIG. 7 presents an expanded view of an outer radial edge of a baffle plate for a plasma processing system according to an embodiment of the present invention;

**[0028]** FIG. 8 presents a method of producing a baffle plate for a plasma processing system according to an embodiment of the present invention;

**[0029]** FIG. 9 presents a method of producing a baffle plate for a plasma processing system according to another embodiment of the present invention; and

**[0030]** FIG. 10 presents a method of producing a baffle plate for a plasma processing system according to another embodiment of the present invention.

#### Detailed Description of an Embodiment

**[0031]** According to an embodiment of the present invention, a plasma processing system 1 is depicted in FIG. 1 comprising a plasma processing chamber 10, an upper assembly 20, an upper electrode 22, a substrate holder 30 for supporting a substrate 35, and a pumping duct 40 coupled to a vacuum pump (not shown) for providing a reduced pressure atmosphere 11 in plasma processing chamber 10. Plasma processing chamber 10 can facilitate the formation of a processing plasma in process space 12 adjacent substrate 35. The plasma processing system 1 can be configured to process 200 mm substrates, 300 mm substrates, or larger.

**[0032]** In the illustrated embodiment, upper electrode 22 comprises an electrode plate 24 (FIG. 1) with a deposition shield 26 (FIG. 1). In an alternate embodiment, upper assembly 20 can comprise at least one of a cover, a gas injection assembly, and an upper electrode impedance match network. For example, the upper electrode 22 can be coupled to an RF source. In another alternate embodiment, the upper assembly 20 comprises a cover coupled to the upper electrode 22, wherein the upper electrode 22 is maintained at an electrical potential equivalent to that of the plasma processing chamber 10. For example, the plasma processing chamber 10, the upper assembly 20, and the upper electrode 22 can be electrically connected to ground potential.

**[0033]** Plasma processing chamber 10 can, for example, further comprise an optical viewport 16 coupled to the deposition shield 26 of upper electrode 22. Optical viewport 16 can comprise an optical window 17 coupled to the backside of an optical window deposition shield 18, and an optical window flange 19 can be configured to couple optical window 17 to the optical window deposition shield 18. Sealing members, such as O-rings, can be provided between the optical window flange 19 and the optical window 17, between the optical window 17 and the optical window deposition shield 18, and between the optical window deposition shield 18 and the plasma processing chamber 10. Optical viewport 16 can, for



example, permit monitoring of optical emission from the processing plasma in process space 12.

**[0034]** Substrate holder 30 can, for example, further comprise a vertical translational device 50 surrounded by a bellows 52 coupled to the substrate holder 30 and the plasma processing chamber 10, and configured to seal the vertical translational device 50 from the reduced pressure atmosphere 11 in plasma processing chamber 10. Additionally, a bellows shield 54 can, for example, be coupled to the substrate holder 30 and configured to protect the bellows 52 from the processing plasma. Substrate holder 10 can, for example, further be coupled to at least one of a focus ring 60, and a shield ring 62. Furthermore, a baffle plate 64 can extend about a periphery of the substrate holder 30.

**[0035]** Substrate 35 can be, for example, transferred into and out of plasma processing chamber 10 through a slot valve (not shown) and chamber feed-through (not shown) via robotic substrate transfer system where it is received by substrate lift pins (not shown) housed within substrate holder 30 and mechanically translated by devices housed therein. Once substrate 35 is received from substrate transfer system, it is lowered to an upper surface of substrate holder 30.

**[0036]** Substrate 35 can be, for example, affixed to the substrate holder 30 via an electrostatic clamping system. Furthermore, substrate holder 30 can, for example, further include a cooling system including a re-circulating coolant flow that receives heat from substrate holder 30 and transfers heat to a heat exchanger system (not shown), or when heating, transfers heat from the heat exchanger system. Moreover, gas can, for example, be delivered to the back-side of substrate 35 via a backside gas system to improve the gas-gap thermal conductance between substrate 35 and substrate holder 30. Such a system can be utilized when temperature control of the substrate is required at elevated or reduced temperatures. In other embodiments, heating elements, such as resistive heating elements, or thermo-electric heaters/coolers can be included.

**[0037]** In the illustrated embodiment, shown in FIG. 1, substrate holder 30 can comprise an electrode through which RF power is coupled to the processing plasma in process space 12. For example, substrate holder 30 can be electrically biased at a RF voltage via the transmission of RF power from a RF generator (not shown) through an impedance match network (not shown) to substrate holder 30.

The RF bias can serve to heat electrons to form and maintain plasma. In this configuration, the system can operate as a reactive ion etch (RIE) reactor, wherein the chamber and upper gas injection electrode serve as ground surfaces. A typical frequency for the RF bias can range from 1 MHz to 100 MHz and is preferably 13.56 MHz. RF systems for plasma processing are well known to those skilled in the art.

**[0038]** Alternately, the processing plasma formed in process space 12 can be formed using a parallel-plate, capacitively coupled plasma (CCP) source, an inductively coupled plasma (ICP) source, any combination thereof, and with and without magnet systems. Alternately, the processing plasma in process space 12 can be formed using electron cyclotron resonance (ECR). In yet another embodiment, the processing plasma in process space 12 is formed from the launching of a Helicon wave. In yet another embodiment, the processing plasma in process space 12 is formed from a propagating surface wave.

**[0039]** Referring now to an illustrated embodiment of the present invention depicted in FIG. 2 (plan view) and FIG. 3 (cross sectional view), baffle plate 64 can form a ring comprising an upper surface 82, a lower surface 84, an inner radial edge 86, and an outer radial edge 88. The baffle plate 64 can further comprise at least one passageway 90 coupled to the upper surface 82 and to the lower surface 84, and configured to permit the flow of gas therethrough.

**[0040]** FIG. 4 provides an expanded view of one of the passageways 90, wherein the expanded view provides a transverse cross sectional view taken along a minor axis of the passageway 90. Each passageway 90 comprises an inner passageway surface 92 contiguous with the upper surface 82 and the lower surface 84 of the baffle plate 64. For example, at least one passageway 90 can comprise a length, dictated by the distance between the upper surface 82 and the lower surface 84 proximate each passageway 90, having a dimensional range from 1 to 50 mm. Desirably, the length comprises a dimensional range from 1 to 10 mm, and preferably the length is at least 5 mm.

**[0041]** FIGs. 5A, 5B, and 5C provide an exemplary cross-sectional view of passageway 90 at the upper surface 82 of baffle plate 64, an additional expanded cross-sectional view of passageway 90 taken along a major axis of the passageway 90, and an exemplary cross-sectional view of passageway 90 at the lower surface 84 of baffle plate 64, respectively.

**[0042]** In the illustrated embodiment shown in FIG. 1 and FIG. 5B, the at least one passageway 90 can comprise slots aligned in a radial direction. In an alternate embodiment of the present invention, the slots can be aligned in an azimuthal direction. In an alternate embodiment of the present invention, the slots can be slanted and, therefore, aligned partially in a radial direction and an azimuthal direction. In an alternate embodiment, the passageways 90 can comprise a combination of alignment methodologies thereof. Alternately, the passageways can include at least one orifice.

**[0043]** Additionally, referring now to FIG. 5A and FIG. 5B, the cross sectional view of the passageway 90 at the upper surface 82 comprises a cross sectional entrance area 91a that is greater than the respective cross sectional exit area 91b depicted in the cross sectional view of the passageway 90 at the lower surface 84. Alternately, the cross sectional area can be, for example, constant along the length of the passageway from the upper surface 82 to the lower surface 84. Alternately, the cross-sectional exit area 91b of the passageway 90 at the lower surface 84 can, for example, comprise a cross-sectional area greater than the respective cross-sectional entrance area 91a of the passageway 90 at the upper surface 82.

**[0044]** Still referring to FIG. 5B and again to FIG. 2, baffle plate 64 can, for example, further comprise a plurality of fastening receptors 100. Each fastening receptor 100 can be coupled to the upper surface 82 and the lower surface 84, and configured to receive fastening devices (not shown) (such as bolts) to couple baffle plate 64 to substrate holder 30. The fastening receptors 100 can comprise a first entrant cavity 102, a second entrant cavity 103, and an exit through-hole 104. Alternately, second entrant cavity 103 is not required. For example, the number of fastening receptors 100 formed within baffle plate 64 can range from 0 to 100. Desirably, the number of fastening receptors 100 ranges from 5 to 20; and, preferably, the number of fastening receptors 100 equals 12 fastening receptors.

**[0045]** FIG. 6 depicts an expanded cross sectional view of the inner radial edge 86 of baffle plate 64 comprising one of the plurality of fastening receptors 100. The inner radial edge 86 can further comprise an inner edge surface 112 and a plurality of fastener mating surfaces 113. The inner edge surface 112 can be coupled to the upper surface 82 and the lower surface 84 of baffle plate 64. At

least one of the fastener mating surfaces 113 can be coupled to and coincident with the lower surface 84 of baffle plate 64 and configured to mate the baffle plate 64 with substrate holder 30. Furthermore, as shown in FIG. 6, the fastening receptor 100 can include an inner receptor surface 106 that can further comprise a first entrant surface 107, a first lip surface 108, a second entrant surface 109, a second lip surface 110, and an exit surface 111. For example, the inner radial edge 86 can comprise a minimum thickness, dictated by a distance between the upper surface 82 and the lower surface 84 proximate the inner radial edge 86, having a dimensional range from 1 to 50 mm. Desirably, the minimum thickness comprises a dimensional range from 1 to 10 mm, and preferably the minimum thickness is at least 2 mm.

**[0046]** FIG. 7 provides an expanded cross sectional view of the outer radial edge 88 of baffle plate 64. The outer radial edge 88 can further comprise an outer edge surface 114, a first mating surface 116, and a second mating surface 118. The outer edge surface 114 can be coupled to the upper surface 82 and the lower surface 84 of baffle plate 64. The first mating surface 116 can be coupled to and coincident with a portion of the upper surface 82, and configured to mate with plasma processing system 1. The second mating surface 118 can be coupled to and coincident with a portion of the lower surface 84, and configured to mate with plasma processing system 1. For example, first and second mating surfaces can be used to mate baffle plate 64 with at least one of a deposition shield 14 and a plasma processing chamber 10. Additionally, for example, the outer radial edge 88 can comprise a thickness, dictated by the distance between the first mating surface 116 and the second mating surface 118 proximate the outer radial edge 88, having a dimensional range from 1 to 50 mm. Desirably, the thickness comprises a dimensional range from 1 to 10 mm, and preferably the thickness is at least 7 mm.

**[0047]** Additionally, as shown in FIG. 2, baffle plate 64 can, for example, further comprise a plurality of mounting through-holes 101. Each mounting through-hole 101 can be coupled to the upper surface 82 and the lower surface 84, and configured to receive fastening devices (not shown) (such as bolts) to couple baffle plate 64 to at least one of the plasma processing chamber 10 and the deposition shield 26 of the upper electrode 22. For example, the number of mounting through-holes 101 formed within baffle plate 64 can range from 0 to 100.

Desirably, the number of mounting through-holes 101 ranges from 5 to 20; and, preferably, the number of mounting through-holes 101 is at least 10.

**[0048]** Referring now to FIGs. 2 through 7, the baffle plate 64 further comprises a protective barrier 150 formed on a plurality of exposed surfaces 145 of the baffle plate 64. In an embodiment of the present invention, the exposed surfaces 145 can comprise the upper surface 82 of baffle plate 64 excluding the first mating surface 116; the lower surface 84 of baffle plate 64 excluding the plurality of fastener mating surfaces 113 and the second mating surface 118; and the inner passageway surface 92 contiguous with the upper surface 82 and the lower surface 84. Additionally, the exposed surfaces 145 can comprise the first entrant surface 107 of each of the plurality of fastening receptors 100; and the first lip surface 108 of each of the plurality of fastening receptors 100. Alternately, the exposed surfaces comprise all surfaces on the baffle plate 64.

**[0049]** In an embodiment of the present invention, the protective barrier 150 can comprise a compound including an oxide of aluminum such as  $\text{Al}_2\text{O}_3$ . In another embodiment of the present invention, the protective barrier 150 can comprise a mixture of  $\text{Al}_2\text{O}_3$  and  $\text{Y}_2\text{O}_3$ . In another embodiment of the present invention, the protective barrier 150 can comprise at least one of a III-column element (column III of periodic table) and a Lanthanon element. In another embodiment of the present invention, the III-column element can comprise at least one of Yttrium, Scandium, and Lanthanum. In another embodiment of the present invention, the Lanthanon element can comprise at least one of Cerium, Dysprosium, and Europium. In another embodiment of the present invention, the compound forming protective barrier 150 can comprise at least one of Yttria ( $\text{Y}_2\text{O}_3$ ),  $\text{Sc}_2\text{O}_3$ ,  $\text{Sc}_2\text{F}_3$ ,  $\text{YF}_3$ ,  $\text{La}_2\text{O}_3$ ,  $\text{CeO}_2$ ,  $\text{Eu}_2\text{O}_3$ , and  $\text{DyO}_3$ .

**[0050]** In an embodiment of the present invention, the protective barrier 150 formed on baffle plate 64 comprises a thermal sprayed coating having a minimum thickness, wherein the minimum thickness can be allowed to vary across the plurality of exposed surfaces 145. In other words, the specified thickness can be variable across the exposed surfaces 145. For example, the minimum thickness can be constant over a first portion of the exposed surfaces 145 and variable over a second portion of the exposed surfaces 145. For example, a variable thickness can occur on a curved surface, on a corner, or in a hole. The minimum thickness ranges from 0 micron to 550 micron. Desirably, the minimum thickness ranges

from 50 micron to 250 micron; and, preferably, the minimum thickness ranges from 150 micron to 250 micron.

**[0051]** FIG. 8 presents a method of producing the baffle plate 64 in the plasma processing system described in FIG. 1 according to an embodiment of the present invention. A flow diagram 300 begins in 310 with fabricating the baffle plate 64 (e.g., a baffle plate having the characteristics of the plate described with reference to FIGs. 2-7). Fabricating the baffle plate can comprise at least one of machining, casting, polishing, forging, and grinding. For example, each of the elements described above can be machined according to specifications set forth on a mechanical drawing, using conventional techniques including a mill, a lathe, etc. The techniques for machining a component using, for example, a mill or a lathe, are well known to those skilled in the art of machining. The baffle plate can, for example, be fabricated from aluminum.

**[0052]** In 320, the baffle plate is anodized to form a surface anodization layer. For example, when fabricating the baffle plate from aluminum, the surface anodization layer comprises aluminum oxide ( $\text{Al}_2\text{O}_3$ ). Methods of anodizing aluminum components are well known to those skilled in the art of surface anodization.

**[0053]** In 330, the surface anodization layer is removed from the exposed surfaces 145 using standard machining techniques. During the same machining step, or during a separate machining step, other surfaces (e.g., the first mating surface of the upper surface, the second mating surface of the lower surface, and the plurality of fastener mating surfaces of the lower surface) may also be machined (e.g., to produce a flat or bare surface that provides at least one of a good mechanical or electrical contact at the machined surface).

**[0054]** In 340, the protective barrier 150 is formed on the exposed surfaces 145. A protective barrier 150 comprising, for example Yttria, can be formed using (thermal) spray coating techniques that are well known to those skilled in the art of ceramic spray coatings. In an alternate embodiment, forming the protective barrier can further comprise polishing the thermal spray coating. For example, polishing the thermal spray coating can comprise the application of sand paper to the sprayed surfaces.

**[0055]** FIG. 9 presents a method of producing the baffle plate in the plasma processing system described in FIG. 1 according to another embodiment of the

present invention. A flow diagram 400 begins in 410 with fabricating the baffle plate 64 (e.g., a baffle plate having the characteristics of the plate described with reference to FIGs. 2-7). Fabricating the baffle plate can comprise at least one of machining, casting, polishing, forging, and grinding. For example, each of the elements described above can be machined according to specifications set forth on a mechanical drawing, using conventional techniques including a mill, a lathe, etc. The techniques for machining a component using, for example, a mill or a lathe, are well known to those skilled in the art of machining. The baffle plate can, for example, be fabricated from aluminum.

**[0056]** In 420, exposed surfaces 145 are masked to prevent the formation of a surface anodization layer thereon. Techniques for surface masking and unmasking are well known to those skilled in the art of surface coatings and surface anodization. During the same masking step, or during a separate masking step, other surfaces (e.g., the first mating surface of the upper surface, the second mating surface of the lower surface, and the plurality of fastener mating surfaces of the lower surface) may also be masked (e.g., to maintain a flat or bare surface that provides at least one of a good mechanical or electrical contact at the machined surface).

**[0057]** In 430, the baffle plate is anodized to form a surface anodization layer on the remaining unmasked surfaces. For example, when fabricating the baffle plate from aluminum, the surface anodization layer can comprise aluminum oxide ( $\text{Al}_2\text{O}_3$ ). Methods of anodizing aluminum components are well known to those skilled in the art of surface anodization.

**[0058]** In 440, the protective barrier 150 is formed on the exposed surfaces. A protective barrier comprising, for example Yttria, can be formed using (thermal) spray coating techniques that are well known to those skilled in the art of ceramic spray coatings. In an alternate embodiment, forming the protective barrier can further comprise polishing the thermal spray coating. For example, polishing the thermal spray coating can comprise the application of sand paper to the sprayed surfaces.

**[0059]** FIG. 10 presents a method of producing the baffle plate in the plasma processing system described in FIG. 1 according to another embodiment of the present invention. A flow diagram 500 begins in 510 with fabricating the baffle plate 64 (e.g., a baffle plate having the characteristics of the plate described with

reference to FIGs. 2-7). Fabricating the baffle plate can comprise at least one of machining, casting, polishing, forging, and grinding. For example, each of the elements described above can be machined according to specifications set forth on a mechanical drawing, using conventional techniques including a mill, a lathe, etc. The techniques for machining a component using, for example, a mill or a lathe, are well known to those skilled in the art of machining. The baffle plate can, for example, be fabricated from aluminum.

**[0060]** In 520, a protective barrier 150 is formed on exposed surfaces 145 of the baffle plate 64. A protective barrier comprising, for example Yttria, can be formed using (thermal) spray coating techniques that are well known to those skilled in the art of ceramic spray coatings. In an alternate embodiment, forming the protective barrier can further comprise polishing the thermal spray coating. For example, polishing the thermal spray coating can comprise the application of sand paper to the sprayed surfaces.

**[0061]** The processes of forming a protective barrier 150 on the exposed surfaces 145, described with reference to FIGs. 8-10 can be modified to utilize a combination of machining and masking. In such a modified process, at least one exposed surface is masked to prevent formation of the anodization layer thereon while other exposed surfaces are anodized. The exposed surfaces that are unmasked are then machined, and the exposed surfaces that were masked are unmasked. The protective barrier 150 can then be formed on all the exposed surfaces. As described above, additional surfaces that are not exposed surfaces may also be machined during the method (e.g., in order to provide a better mechanical or electrical contact than would be formed with the anodization layer thereon).

**[0062]** Although only certain exemplary embodiments of this invention have been described in detail above, those skilled in the art will readily appreciate that many modifications are possible in the exemplary embodiments without materially departing from the novel teachings and advantages of this invention. Accordingly, all such modifications are intended to be included within the scope of this invention.



**CLAIMS:**

What is claimed is:

1. An improved baffle plate in a plasma processing system comprising:  
a ring comprising an upper surface, a lower surface, an inner radial edge coupled to said upper surface and said lower surface, an outer radial edge coupled to said upper surface and said lower surface, and at least one passageway coupled to said upper surface and to said lower surface and configured to permit the flow of a gas therethrough, wherein said upper surface comprises a first mating surface proximate said outer radial edge, said lower surface comprises a second mating surface proximate said outer radial edge and a plurality of fastener mating surfaces proximate said inner radial edge, and each of said at least one passageway comprises an internal passageway surface; and  
a protective barrier coupled to a plurality of exposed surfaces of said baffle plate, wherein said exposed surfaces comprise said upper surface excluding said first mating surface, said lower surface excluding said second mating surface and said plurality of fastener mating surfaces, and said internal passageway surface of each of said at least one passageway.
2. The improved baffle plate as recited in claim 1, wherein at least one passageway comprises a slot.
3. The improved baffle plate as recited in claim 2, wherein said slot comprises an entrance area and an exit area, wherein said entrance area is larger than said exit area.
4. The improved baffle plate as recited in claim 1, wherein at least one passageway comprises an orifice.
5. The improved baffle plate as recited in claim 1, wherein said ring further comprises at least one of a plurality of fastening receptors and a plurality of mounting through-holes coupled to said upper surface and said lower surface of said baffle plate and configured to receive fastening devices in order to couple said baffle plate to said plasma processing system.

6. The improved baffle plate as recited in claim 5, wherein at least one of said plurality of fastening receptors comprises an entrant cavity, an exit through-hole, and an inner receptor surface.

7. The improved baffle plate as recited in claim 6, wherein said inner receptor surface of at least one of said plurality of fastening receptors comprises a first entrant surface, a first lip surface, a second entrant surface, a second lip surface, and an exit surface.

8. The improved baffle plate as recited in claim 7, wherein said plurality of exposed surfaces further comprises said first entrant surface and said first lip surface.

9. The improved baffle plate as recited in claim 1, wherein said baffle plate comprises a metal.

10. The improved baffle plate as recited in claim 9, wherein said metal comprises aluminum.

11. The improved baffle plate as recited in claim 1, wherein said protective barrier comprises a compound containing at least one of a III-column element and a Lanthanone element.

12. The improved baffle plate as recited in claim 11, wherein said III-column element comprises at least one of Yttrium, Scandium, and Lanthanum.

13. The improved baffle plate as recited in claim 11, wherein said Lanthanone element comprises at least one of Cerium, Dysprosium, and Europium.

14. The improved baffle plate as recited in claim 11, wherein said compound comprises at least one of Yttria ( $Y_2O_3$ ),  $Sc_2O_3$ ,  $Sc_2F_3$ ,  $YF_3$ ,  $La_2O_3$ ,  $CeO_2$ ,  $Eu_2O_3$ , and  $DyO_3$ .

15. The improved baffle plate as recited in claim 1, wherein said protective barrier comprises a minimum thickness and said minimum thickness is constant across at least one of said plurality of exposed surfaces.

16. The improved baffle plate as recited in claim 1, wherein said protective barrier comprises a minimum thickness and said minimum thickness is variable across at least one of said plurality of exposed surfaces.

17. A method of producing an improved baffle plate of a plasma processing system, said method comprising the steps:

    fabricating said baffle plate, said baffle plate comprising an upper surface, a lower surface, an inner radial edge, an outer radial edge, and at least one passageway coupled to said upper surface and to said lower surface and configured to permit the flow of a gas therethrough, wherein a portion of said upper surface comprises a first mating surface proximate said outer radial edge, a portion of said lower surface comprises a second mating surface proximate said outer radial edge and a plurality of fastener mating surfaces proximate said inner radial edge, and each of said at least one passageway comprises an internal passageway surface; and

    forming a protective barrier on a plurality of exposed surfaces, said exposed surfaces comprising said upper surface of said baffle plate excluding said first mating surface; said lower surface of said baffle plate excluding said plurality of fastener mating surfaces and said second mating surface; and said inner passageway surface coupled to said upper surface and said lower surface.

18. The method as recited in claim 17, wherein said method further comprises the steps:

    anodizing said baffle plate to form a surface anodization layer on said baffle plate; and

    machining said exposed surfaces on said baffle plate to remove said surface anodization layer.

19. The method as recited in claim 17, wherein said method further comprises the steps:

masking said exposed surfaces to prevent formation of a surface anodization layer;

anodizing said baffle plate to form a surface anodization layer on said baffle plate; and

unmasking said exposed surfaces on said baffle plate.

20. The method as recited in claim 17, wherein said fabricating comprises at least one of machining, casting, polishing, forging, and grinding.

21. The method as recited in claim 17, wherein said forming said protective barrier further comprises polishing said protective barrier on at least one of said exposed surfaces.

22. The method as recited in claim 17, wherein said baffle plate further comprises at least one of a plurality of fastening receptors and a plurality of mounting through-holes coupled to said upper surface and said lower surface and configured to receive fastening devices in order to couple said baffle plate to said plasma processing system.

23. The method as recited in claim 22, wherein each of said plurality of fastening receptors comprises an entrant cavity, an exit through-hole, and an inner receptor surface.

24. The method as recited in claim 23, wherein said inner receptor surface of each of said plurality of fastening receptors comprises a first entrant surface, a first lip surface, a second entrant surface, a second lip surface, and an exit surface.

25. The method as recited in claim 24, further comprising machining said first entrant surface, said first lip surface, said second entrant surface, said second lip surface, and said exit surface.

26. The method as recited in claim 24, further comprising forming the protective barrier on said first entrant surface and said first lip surface.

27. The method as recited in claim 17, wherein said baffle plate comprises a metal.

28. The method as recited in claim 27, wherein said metal comprises aluminum.

29. The method as recited in claim 17, wherein said protective barrier comprises a compound containing at least one of a III-column element and a Lanthanoid element.

30. The method as recited in claim 29, wherein said III-column element comprises at least one of Yttrium, Scandium, and Lanthanum.

31. The method as recited in claim 29, wherein said Lanthanoid element comprises at least one of Cerium, Dysprosium, and Europium.

32. The method as recited in claim 17, wherein said protective barrier comprises at least one of Yttria ( $Y_2O_3$ ),  $Sc_2O_3$ ,  $Sc_2F_3$ ,  $YF_3$ ,  $La_2O_3$ ,  $CeO_2$ ,  $Eu_2O_3$ , and  $DyO_3$ .

33. The method as recited in claim 17, wherein said protective barrier comprises a minimum thickness and said minimum thickness is constant across at least one of said exposed surfaces.

34. The method as recited in claim 17, wherein said protective barrier comprises a minimum thickness and said minimum thickness is variable across at least one of said exposed surfaces.

35. The method as recited in claim 17, wherein said exposed surfaces further comprise all surfaces remaining on said baffle plate.

36. A method of producing an improved baffle plate of a plasma processing system, said method comprising the steps:

fabricating said baffle plate, said baffle plate comprising an upper surface, a lower surface, an inner radial edge, an outer radial edge, and at least one passageway coupled to said upper surface and to said lower surface and configured to permit the flow of a gas therethrough, wherein a portion of said upper surface comprises a first mating surface proximate said outer radial edge, a portion of said lower surface comprises a second mating surface proximate said outer radial edge and a plurality of fastener mating surfaces proximate said inner radial edge, and each of said at least one passageway comprises an internal passageway surface;

anodizing said baffle plate to form a surface anodization layer on said baffle plate;

machining exposed surfaces on said baffle plate to remove said surface anodization layer, said exposed surfaces comprising said upper surface of said baffle plate excluding said first mating surface; said lower surface of said baffle plate excluding said plurality of fastener mating surfaces and said second mating surface; and said inner passageway surface coupled to said upper surface and said lower surface; and

forming a protective barrier on said exposed surfaces.

37. The method as recited in claim 36, wherein said baffle plate further comprises at least one of a plurality of fastening receptors and a plurality of mounting through-holes coupled to said upper surface and said lower surface and configured to receive fastening devices in order to couple said baffle plate to said plasma processing system.

38. The method as recited in claim 37, wherein each of said plurality of fastening receptors comprises an entrant cavity, an exit through-hole, and an inner receptor surface.

39. The method as recited in claim 38, wherein said inner receptor surface of each of said plurality of fastening receptors comprises a first entrant surface, a first lip surface, a second entrant surface, a second lip surface, and an exit surface.

40. The method as recited in claim 39, further comprising machining said first entrant surface, said first lip surface, said second entrant surface, said second lip surface, and said exit surface.

41. The method as recited in claim 39, further comprising forming the protective barrier on said first entrant surface and said first lip surface.

42. The method as recited in claim 36, wherein said protective barrier comprises a compound containing at least one of a III-column element and a Lanthanoid element.

43. The method as recited in claim 42, wherein said III-column element comprises at least one of Yttrium, Scandium, and Lanthanum.

44. The method as recited in claim 42, wherein said Lanthanoid element comprises at least one of Cerium, Dysprosium, and Europium.

45. The method as recited in claim 36, wherein said protective barrier comprises at least one of Yttria ( $Y_2O_3$ ),  $Sc_2O_3$ ,  $Sc_2F_3$ ,  $YF_3$ ,  $La_2O_3$ ,  $CeO_2$ ,  $Eu_2O_3$ , and  $DyO_3$ .

46. A method of producing an improved baffle plate of a plasma processing system, said method comprising the steps:

fabricating said baffle plate, said baffle plate comprising an upper surface, a lower surface, an inner radial edge, an outer radial edge, and at least one passageway coupled to said upper surface and to said lower surface and configured to permit the flow of a gas therethrough, wherein a portion of said upper surface comprises a first mating surface proximate said outer radial edge, a portion of said lower surface comprises a second mating surface proximate said outer radial edge and a fastener mating surface proximate said inner radial edge, and each of said at least one passageway comprises an internal passageway surface;

masking exposed surfaces on said baffle plate to prevent formation of a surface anodization layer, said exposed surfaces comprising said upper surface of

said baffle plate excluding said first mating surface; said lower surface of said baffle plate excluding said plurality of fastener mating surfaces and said second mating surface; and said inner passageway surface coupled to said upper surface and said lower surface;

anodizing said baffle plate to form said surface anodization layer on said baffle plate;

unmasking said exposed surfaces; and

forming a protective barrier on said exposed surfaces.

47. The method as recited in claim 46, wherein said baffle plate further comprises at least one of a plurality of fastening receptors and a plurality of mounting through-holes coupled to said upper surface and said lower surface and configured to receive fastening devices in order to couple said baffle plate to said plasma processing system.

48. The method as recited in claim 47, wherein each of said plurality of fastening receptors comprises an entrant cavity, an exit through-hole, and an inner receptor surface.

49. The method as recited in claim 48, wherein said inner receptor surface of each of said plurality of fastening receptors comprises a first entrant surface, a first lip surface, a second entrant surface, a second lip surface, and an exit surface.

50. The method as recited in claim 49, further comprising machining said first entrant surface, said first lip surface, said second entrant surface, said second lip surface, and said exit surface.

51. The method as recited in claim 49, further comprising forming the protective barrier on said first entrant surface and said first lip surface.

52. The method as recited in claim 46, wherein said protective barrier comprises a compound containing at least one of a III-column element and a Lanthanum element.



53. The method as recited in claim 52, wherein said III-column element comprises at least one of Yttrium, Scandium, and Lanthanum.

54. The method as recited in claim 52, wherein said Lanthanoid element comprises at least one of Cerium, Dysprosium, and Europium.

55. The method as recited in claim 46, wherein said protective barrier comprises at least one of Yttria ( $\text{Y}_2\text{O}_3$ ),  $\text{Sc}_2\text{O}_3$ ,  $\text{Sc}_2\text{F}_3$ ,  $\text{YF}_3$ ,  $\text{La}_2\text{O}_3$ ,  $\text{CeO}_2$ ,  $\text{Eu}_2\text{O}_3$ , and  $\text{DyO}_3$ .



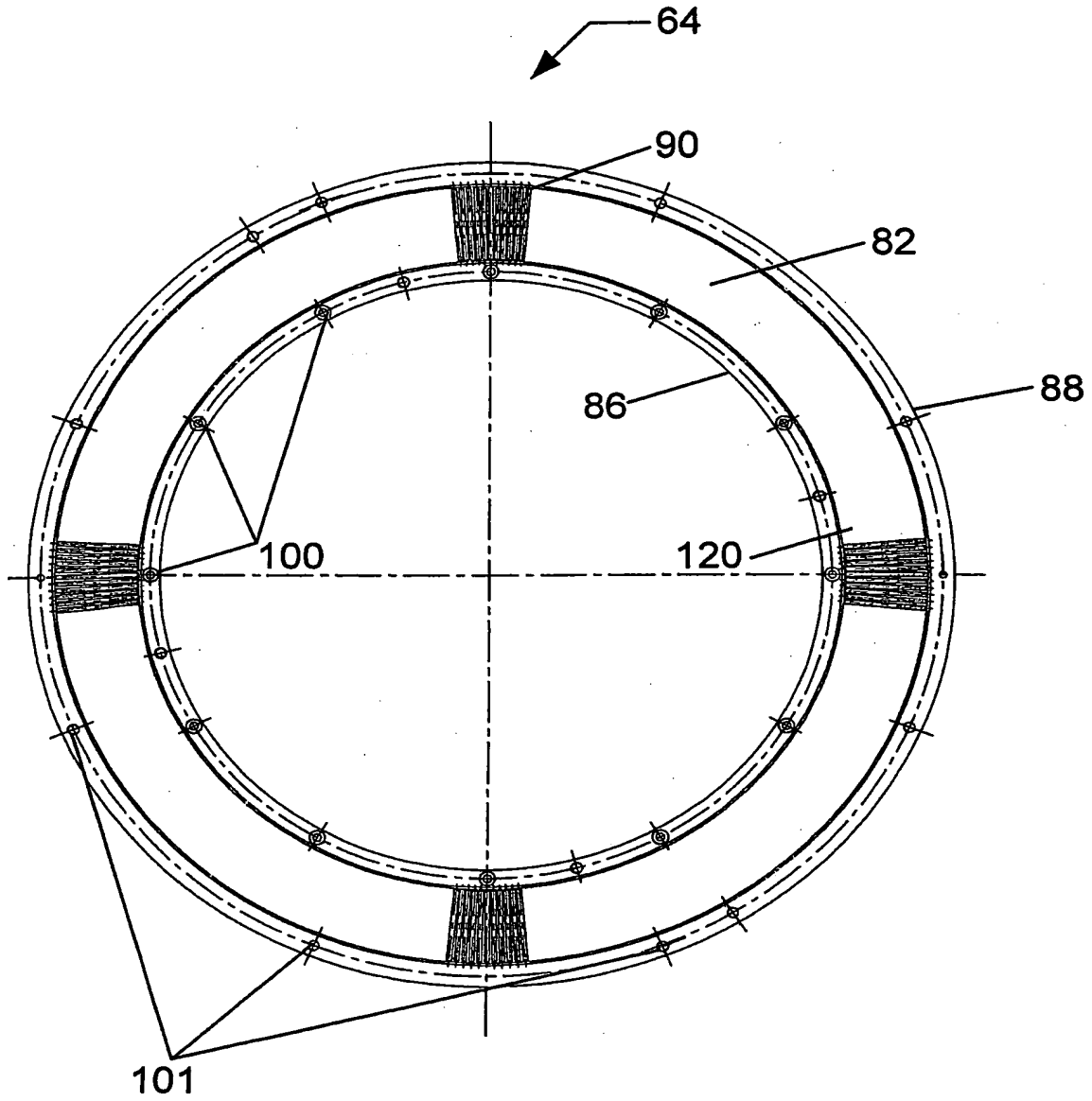


FIG. 2.

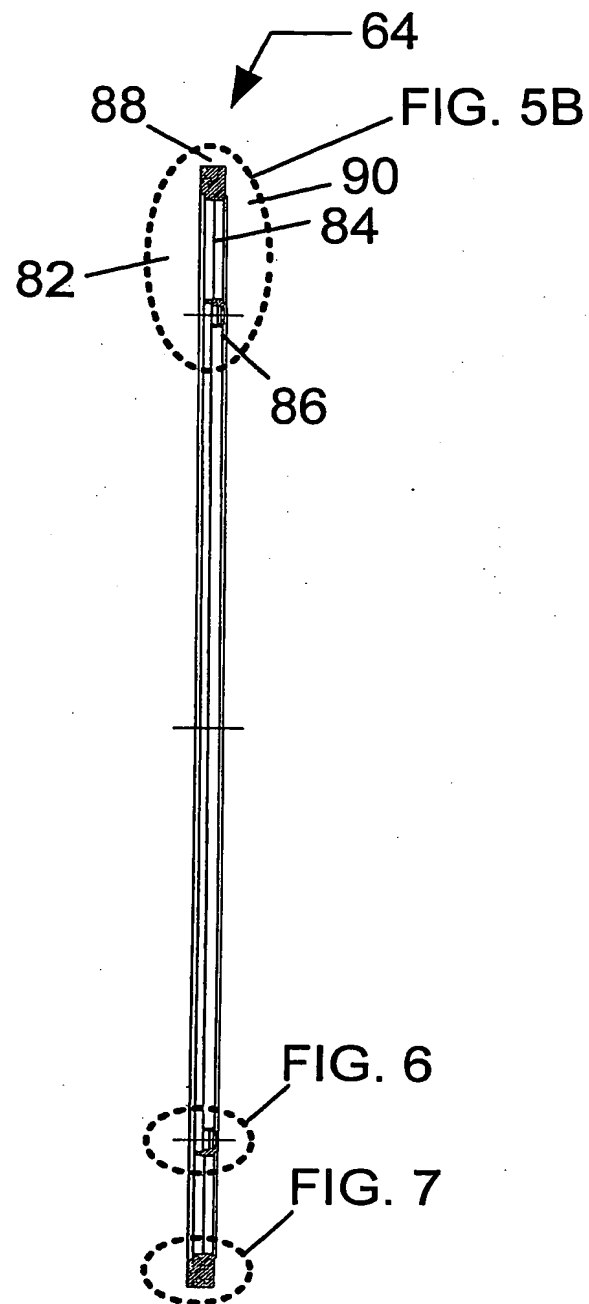


FIG. 3.

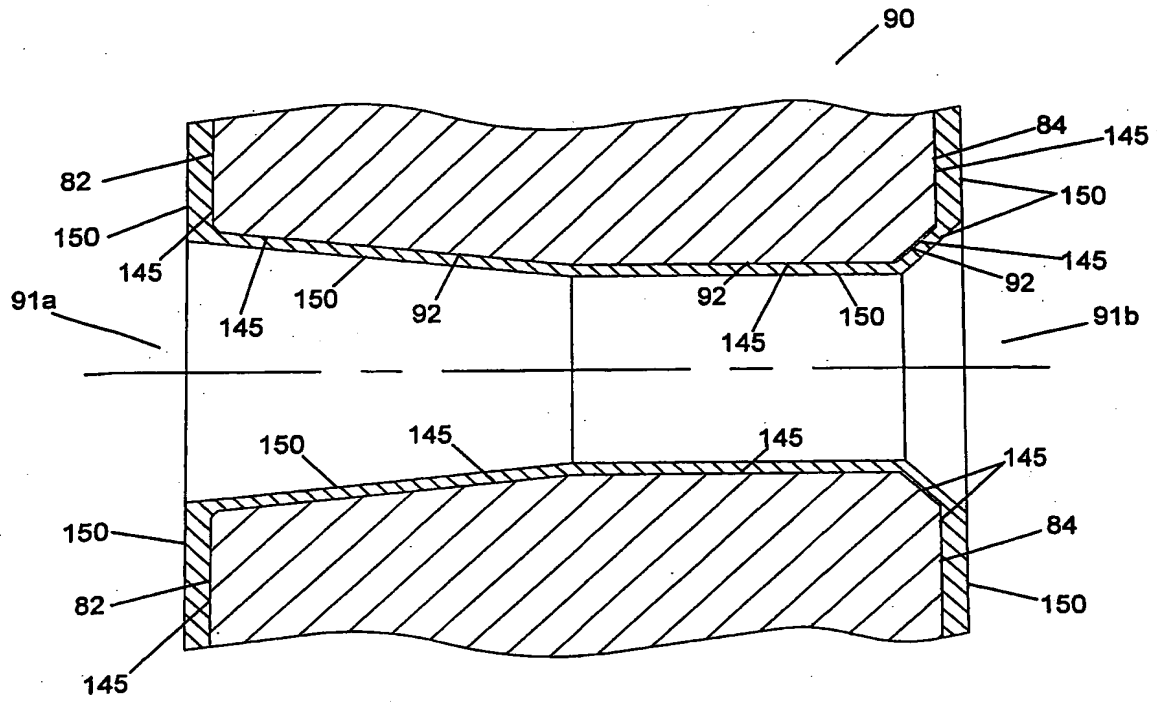


FIG. 4.

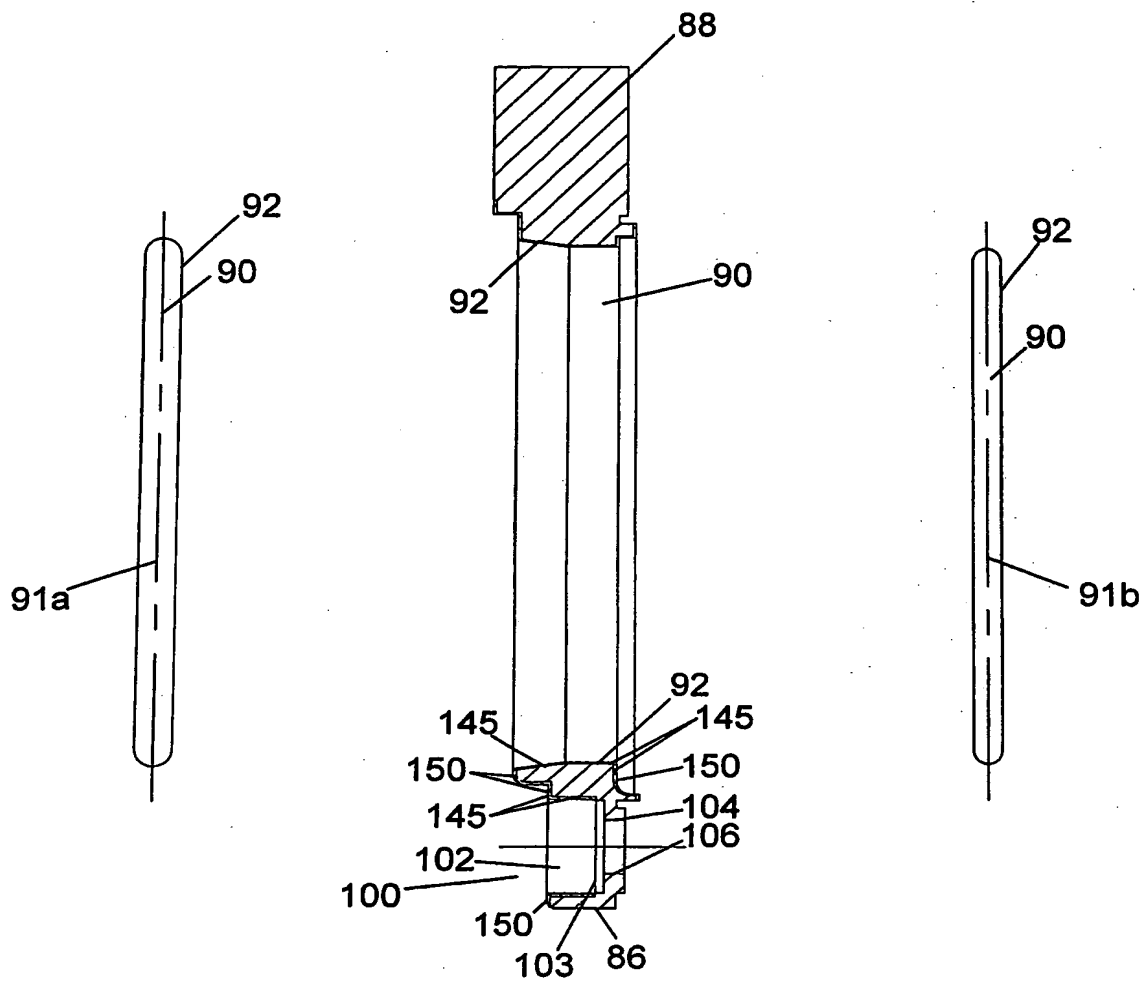


FIG. 5A.

FIG. 5B.

FIG. 5C.

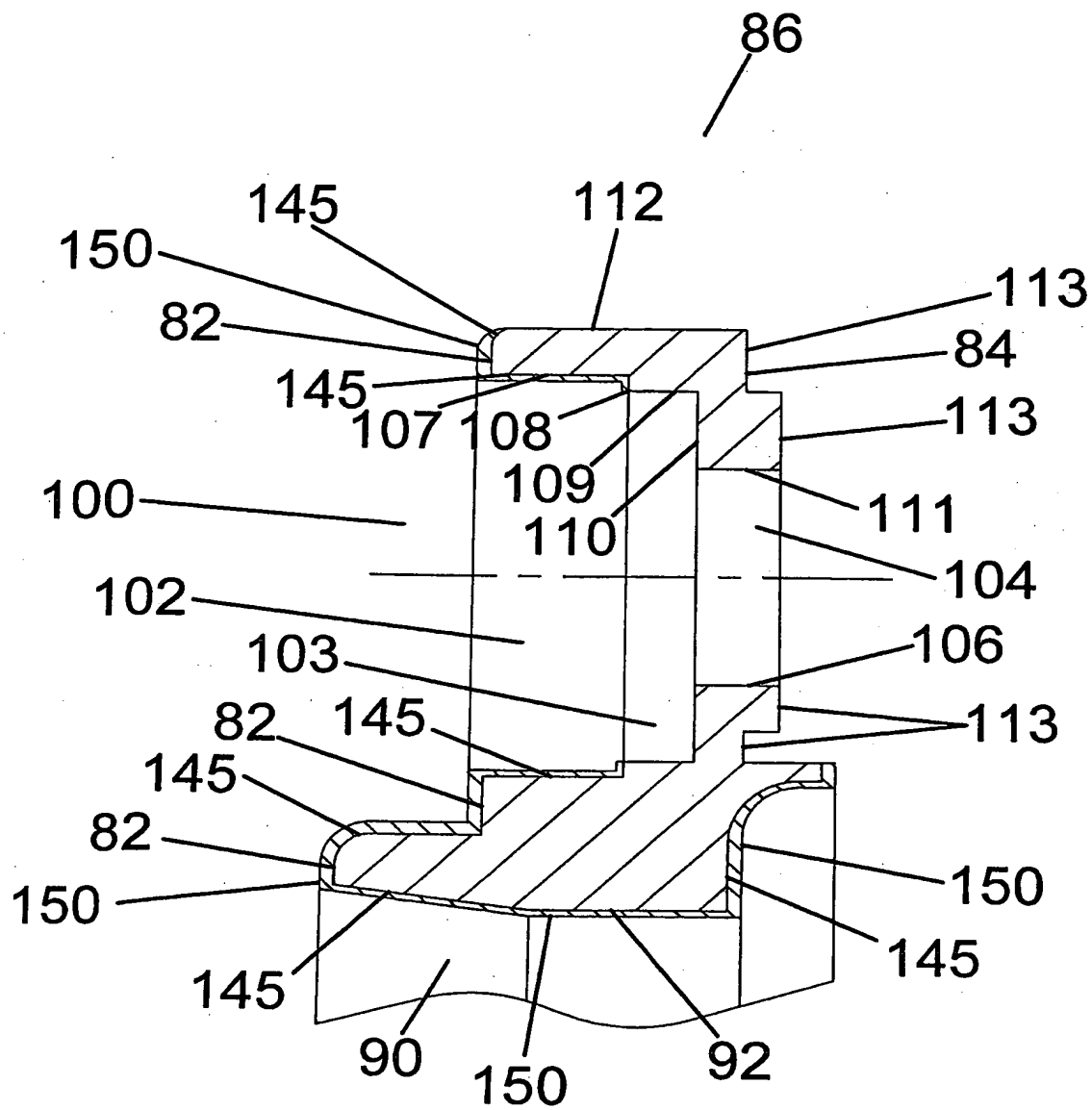


FIG. 6.

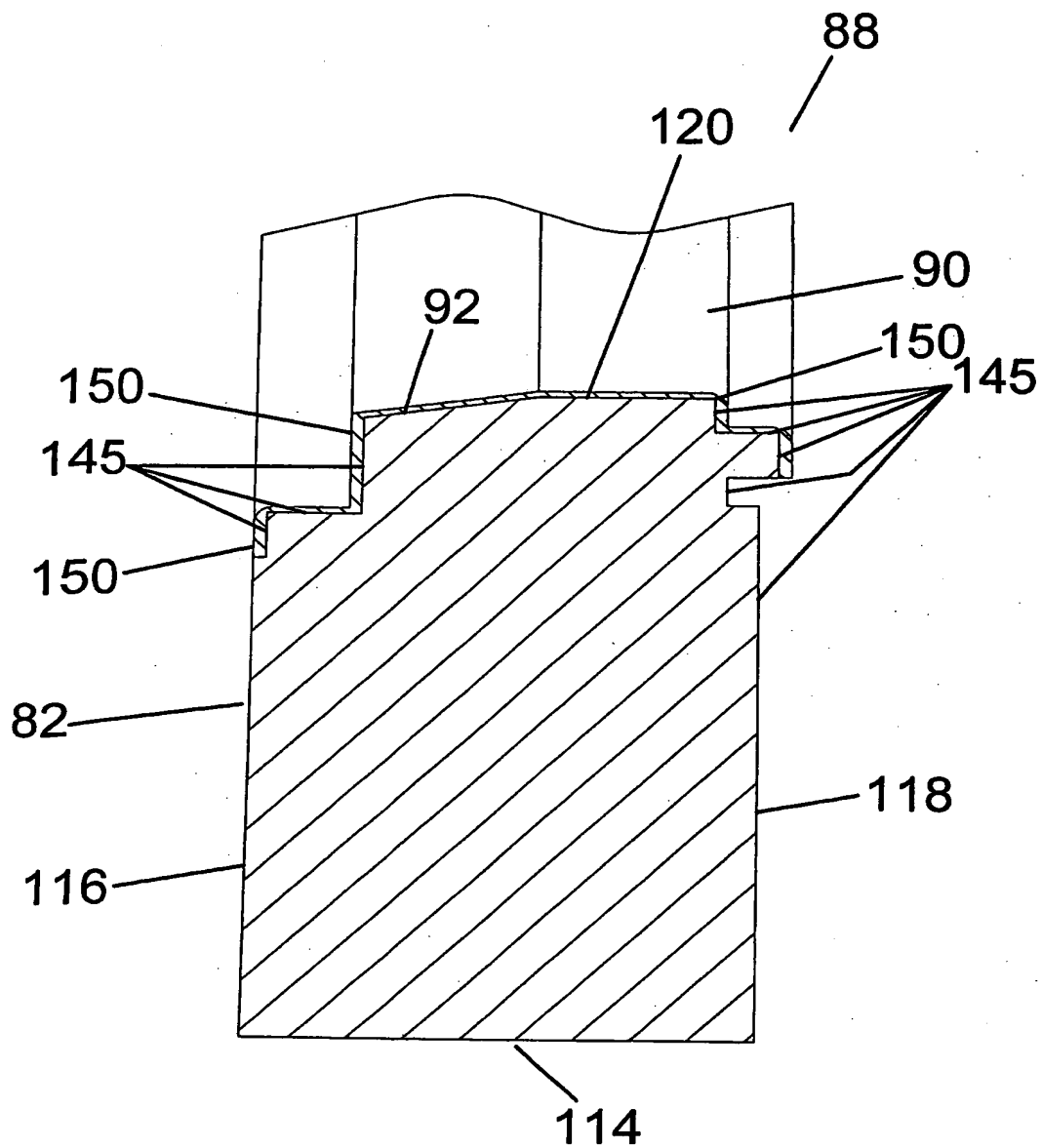


FIG. 7.



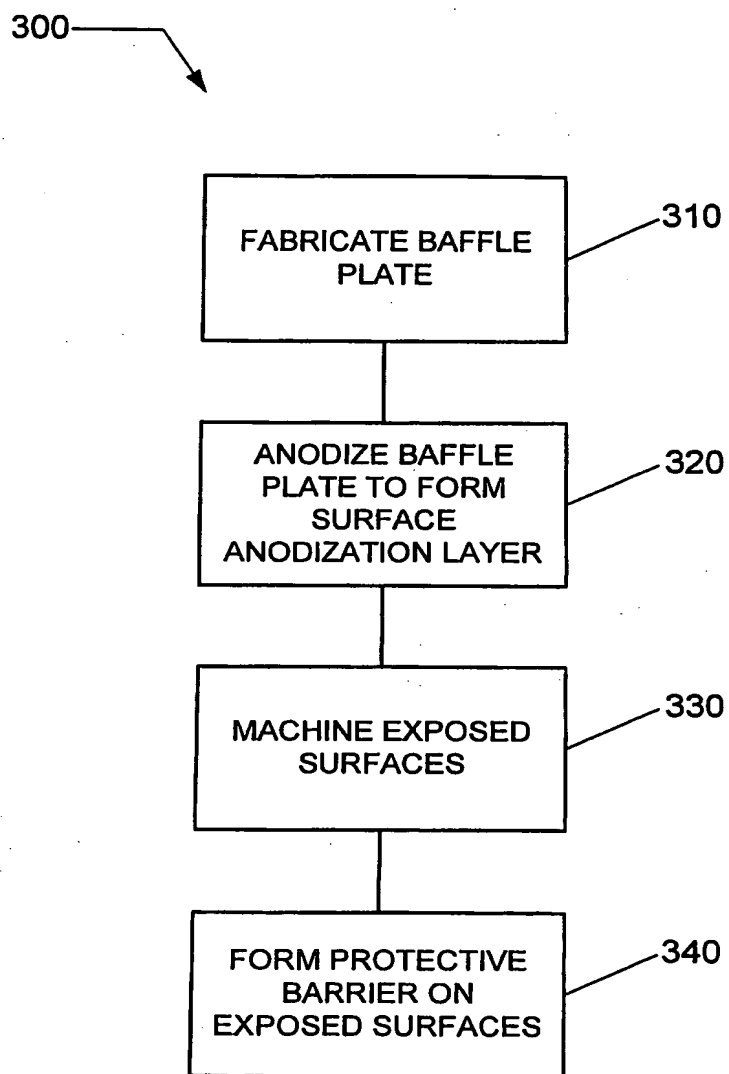


FIG. 8.

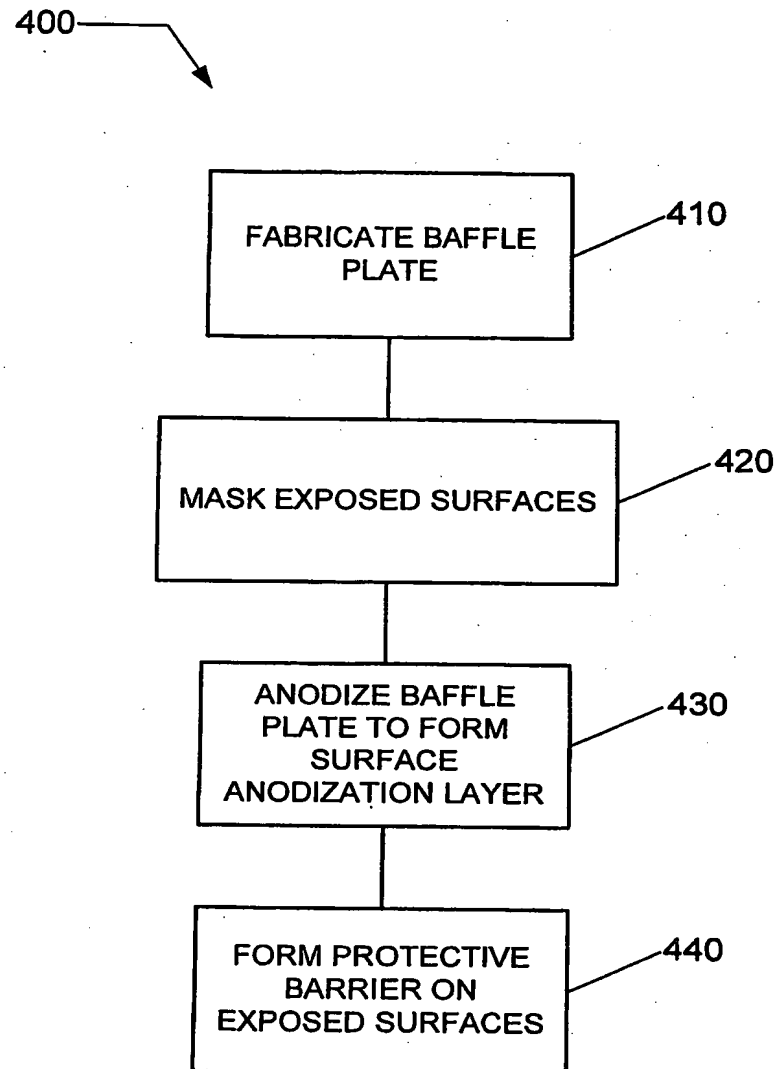


FIG. 9.

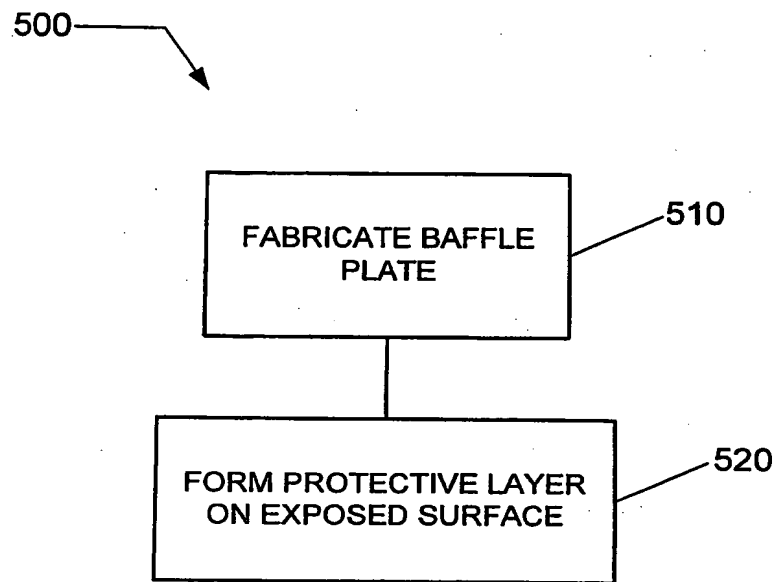


FIG. 10.

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